



Sheet 1 of 1

Form PTO-1449		Docket No. P03,0158		Serial No. 10/614,280			
INFORMATION DISCLOSURE CITATION IN AN APPLICATION (use several sheets if necessary)				Applicant Tilman Schilling et al.			
				Filing Date 07/07/2003			
				Group Art Unit 2854			
U.S. PATENT DOCUMENTS							
Examiner's Initials		Document Number	Date	Name	Class	Subclass	Filing Date If appropriate
WH	AA	6,041,704	03/28/2000	Pauschinger	101	91	
WH	AB	5,953,426	09/14/99	Windel et al.	380	51	
WH	AC	5,790,768	08/04/98	Windel et al.	358	1.18	
WH	AD	5,710,721	01/20/98	Rieckoff et al.	714	51	
WH	AE	5,707,158	01/13/98	Hansel et al.	400	61	
WH	AF	5,651,103	07/22/97	Arsenault et al.	358	1.18	
WH	AG	5,608,636	03/04/97	Guenther	705	408	
WH	AH	5,155,499	10/13/92	Goetz et al.	347	12	
WH	AI	5,117,374	05/26/92	Goetz	702	150	
WH	AJ	4,978,971	12/18/90	Goetz et al.	347	5	
	AK						
FOREIGN PATENT DOCUMENTS							
		Document Number	Date	Country	Class	Subclass	Translation Yes No
WH	AL	1 176 016	01/30/2002	Europe			
WH	AM	1 154 382	11/14/2001	Europe			
WH	AN	1 132 868	09/12/2001	Europe			
WH	AO	921 009	06/09/99	Europe			
WH	AP	921 008	06/09/99	Europe			
WH	AQ	762 334	03/12/97	Europe			
	AR						
	AS						
OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)							
WH	AT	XILINX, "Spartan-II 2.5 V FPGA Family: Functional Description" March 5, 2001, pgs 1-45					
	AU						
	AV						
	AW						
Examiner		Date Considered					
WASSEEM H. HANAW		12/30/03					
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							